Amendments to the Specification

Amend the following numbered paragraphs of the specification:

[0056] In this manner, during a normal operation, the master latch circuit 3 master latch circuit 2 in the transparent latch circuit 10 serves as a latch circuit and the received signal ΦM is passed through the slave latch circuit 3. Therefore, overall, the transparent latch circuit 10 functions as a latch circuit.

Docket No. JP920030086US1 SN: 10/710,185